PAPI (Performance Application Programming Interface) provides the tool designer and application engineer with a consistent interface and methodology for use of the performance counter hardware found in most major microprocessors. In addition, it provides access to a collection of components that expose performance measurement opportunities across the hardware and software stack.

**PERFORMANCE ANALYSIS TOOLS**

- Vampir
- TAU
- HPCView

**STANDARD FEATURES**

- Standardized Performance Metrics
- Easy Access to Platform-Specific Metrics
- Multiplexed Event Measurement
- Dispatch on Overflow
- Overflow & Profiling on Multiple Simultaneous Events
- Bindings for C, Fortran and Matlab
- User Definable Metrics derived from Platform-Specific Metrics
- Support for Virtual Computing Environments
- Performance counter monitoring at task granularity for dataflow runtime PaRSEC

**SUPPORTED ARCHITECTURES**

- **AMD**
  - Cortex A8, A9, A15, X-Gene (ARM64)

- **Cray**
  - Blue Gene Series, EMON power on BG/Q
  - Power Series

- **IBM**
  - Blue Gene Series, EMON power on BG/Q
  - Power Series

- **Infiniband**
  - Nehalem, Westmere, Sandy Bridge, Ivy Bridge, Haswell, Knights Corner
  - Coming Soon: Broadwell
  - RAPL, Power on Xeon Phi

- **NVidia**
  - Tesla, Kepler, NVML

**FUTUREGRID**

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**FIND OUT MORE AT** http://icl.cs.utk.edu/papi